NALLA NARASIMHA REDDY

Education Society's Group of Institutions–Integrated Campus

(Approved by AICTE & PCI, New Delhi & Affiliated to JNTUH, Accredited by NAAC with A+ Grade)

Chowdariguda (V), Korremula 'X' Road, Ghatkesar (M), Medchal-Malkajgiri (D), Hyderabad - 500088, Telangana.

(UGC AUTONOMOUS INSTITUTION)

NNRG Integrated Campus

Accredited by A+ NAAC · AICTE · ATAL

Department of Electronics & Communication Engineering

Organizes

A One Week Offline Faculty Development Program on

"Futuristic Frontiers: AI & IoT Integration for advancing Signal Processing Development"

Dr. G. Mallikarjuna Rao
SC- G , Group Director
RCI/DRDO , Hyderabad

Dr. C. V. Krishna Reddy
Director, NNRG
Hyderabad

Dr. C. Krishna Mohan
Professor & Dean
IIT, Hyderabad

Dr. T. Kishore Kumar
Senior Professor, ECE
NIT Warangal

Dr. P. Suryanarayana
Scientist F & DGMDRDO (Rtd.)
Hyderabad

Dr. J. Ravi Kumar
Professor, ECE
NIT Warangal

Dr. I. Earnest Paul
Assistant Professor, ECE
NIT Warangal

Dr. Rahul Jasvantbhai Pandya
Assistant Professor
IIT, Dharwad

Dr. R. S. Sairam
Director, LimonFLIP
Solutions, Hyderabad

Sponsored by: AICTE - ATAL

1st to 6th December 2025

Contact: 9030717749

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No Registration Fee

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Dr. Ravi Bolimera
HOD, ECE, NNRG

Co-coordinator
Dr. Shaik Fairooz
Assoc. Prof. ECE, NNRG

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Dr. Sravan Kumar Vittapu, Associate Professor
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ABOUT FDP
This FDP will provide an opportunity to learn recent research and development in the area of Artificial Intelligence and Internet of Things for building signal processing applications. The participants will have exposure to the state-of-the-art research perspectives. Signal processing: a field at the heart of science and everyday life. The notion of "signal processing" might seem like something impenetrably complex, even to scientists. Acquiring, shaping and transforming data, cleaning it for the sake of improved analysis and extraction of useful information – all of this is what experimental science is about.The aim of the FDP is to contribute to the cross fertilization between the research on AI algorithms, IoT and their applications to Signal Processing.

Objectives
The objectives of this Faculty Development Program (FDP) are designed
➢ To enhance the knowledge, skills, and research capabilities of faculty members in the rapidly evolving fields of Artificial Intelligence (AI) and the Internet of Things (IoT).
➢ To provide a forum to exchange views, ideas & innovations in the field of Signal Processing.
➢ To offer learning on basics, emerging trends & challenges in use of AI and IoT.
➢ To improve faculty's ability in carrying out research&innovations in this area.
➢ To introduce advanced algorithms and to provide opportunities to further widen the knowledge and to pursue research.
➢ To provide hands-on training using various tools.

Expected Outcome
➢ Participants will get in depth understanding of trending technologies 'AI and Internet of Things'.
➢ Use of descriptive and predictive tools & using robust analytical software's.
➢ Gain better understanding on research how to model, analyze and develop robust systems.
➢ To make the participants familiar with trending research problems and NEP-2020.

**A One - Week off-line
Faculty Development Programme
on
"Futuristic Frontiers: AI &IoT
Integration for advancing
Signal Processing Development"**

01ˢᵗ to 06ᵗʰ December 2025

SPONSORED BY

ANNOUNCEMENT BROCHURE

Program Coordinator
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ORGANIZED BY

**Department of
Electronics & Communication Engineering**

**NALLA NARASIMHA REDDY EDUCATION
SOCIETY'S GROUP OF INSTITUTIONS
INTEGRATED CAMPUS
(AUTONOMOUS)**
Chowdariguda, Medchal (Dist),Hyderabad. – 500088

Preamble

AICTE is committed for the development of qualitytechnical education in the country by initiating variousschemes launched by Govt. of India, MHRD.The Councilunderstands that it is the need of the day to train the younggeneration in skill sector and faculty & technicians in theirrespective disciplines. The National Education Policy (NEP) of 2020 serves as a guiding light for this transformative journey. To keep an institute competitive andmore productive, faculty training on the latest tools andtechnologies is vital. Training is required for increasing theknowledge and skills of students to make them moreemployable to acquire global competencies. Accordingly, itwas decided to the formation of AICTE Training AndLearning - (ATAL) Academies.

About the Institution

Nalla Narasimha Reddy Education Society is established and registered with a primary objective of educating young men and women; to prepare them for fast changing national and international requirements; to fund the needy and promote research in the area of science and technology. Accordingly, an Integrated Contiguous Campus with multi-disciplinary Technical, Engineering, Pharmacy and Management programs under the banner of Nalla Narasimha Reddy Education Society's (NNRG) Group of Institutions is started. NNRG was established in the year **2009**, affiliated to JNTU – Hyderabad, Approved by AICTE – New Delhi and PCI - New Delhi. Institution established with sole objective of providing world class technical education.

This Institute has conferred Autonomous status by University Grants Commission **(UGC)** for 10 years in the year 2021and is accredited by the National Assessment and Accreditation Council **(NAAC)** A+ and National Board of Accreditation **(NBA)** for CSE, ECE. The UGC has recognized the college under **section 2(f)** of the UGC Act 1956 and **ISO 9001:2015** certified.

NNRG aims at being one of the most premier educational institutions in Telangana. Our plans are to produce the best quality students who would contribute optimum to the society, nation and the global world with latest trends and innovations. We intend to establish a system of Continuous Quality Improvement (CQI) processes that would evaluate the quality of education and training and improve the teaching-learning methods to ultimately lead the Institute to Excellence.

About ECE Department

Electronics is the branch of study that has revolutionized the life style of humanity. It is a pace setter and a prime mover behind the transition to a technological society. The field of Electronics and Communication Engineering encompasses all areas of human life. The Department of Electronics & Communication Engineering was started in the year 2009. It is a full-fledged academic department for emerging ECE engineers.

M.Tech. (Embedded systems and VLSI) and B.Tech. (ECE) programs were duly approved by the AICTE and affiliated to JNTU, Hyderabad With an intake of 18 and 180 seats respectively. The Department is accredited by National Board of Accreditation (NBA) in the year 2018. The department has spacious laboratories and state-of-the-art equipment for various laboratories. Seminars, Guest Lectures, Workshops and Technical Symposia are conducted in the department to keep faculty and students updated with latest developments in various technologies.

The department is committed to provide students the opportunity to embark on a rewarding and exciting learning journey in preparation for a future career. Department is making exhilarating progress in areas ranging from signal and Image Processing, microelectronics, mobile communications to VLSI Design Automation. Students draw the expertise and knowledge of our faculty by interacting them in class rooms & Laboratories. The team approach is very warm in the Department of ECE.

Resource Persons

Resource persons are identified from highly reputed Institutions and Industry across India such as IITs / NITs / IIITs and industries.

Eligibility

The faculty members of the AICTE approved institutions, research scholars, professionals from industry and staff of host institutions. **ATAL FDPs are completely free for participants.**

Registration link -
https://atalacademy.aicte-india.org/login

BASIC AICTE ATAL FDP/CPDP Week 1 - A Typical Flow Week 1 – Offline (09:30 am – 5:30 am)					
Day 1	Day 2	Day 3	Day 4	Day 5	Day 6
9:00 – 9:30 Inauguration					
9:30 – 12:00 Session 1	9:30 – 12:00 Session 3	9:30 – 12:00 Session 5	9:30 – 12:00 Session 7	9:30 – 1:00 Industrial visit	9:30 – 12:00 Session 10
12:00 – 1:00 Article Discussion	12:00 – 1:00 Article Discussion	12:00 – 1:00 Article Discussion	12:00 – 1:00 Article Discussion		12:00 – 1:00 Article Summary
1:00 – 2:00 Lunch	1:00 – 2:00 Lunch	1:00 – 2:00 Lunch	1:00 – 2:00 Lunch	1:00 – 2:00 Lunch	1:00 – 2:00 Lunch
2:00 – 4:30 Session 2	2:00 – 4:30 Session 4	2:00 – 4:30 Session 6	2:00 – 4:30 Session 8	2:00 – 4:30 Session 9	2:00 – 4:00 MCQ& Reflection Journal
4:30 – 5:30 Hands on training / Labs	4:30 – 5:30 Hands on training / Labs	4:30 – 5:30 Hands on training / Labs	4:30 – 5:30 Hands on training / Labs	4:30 – 5:30 Hands on training / Labs	4:00 – 5:00 Valedictory Session

Certification

Candidates would be eligible to receive a certificate up on achieving at least 70% cumulative weightage in the following aspects in the weightage mentioned.

Attendance – minimum 80% attendance is mandatory.	20
One assessment, - combination of MCQs/short answer type/reasoning based	10
2 Page Article Summary/per Team/ individual.	30
Output of practical sessions	15
Report/outcome of Industrial visit- (Team).	10
Reflection Journal - (Individual).	15